

ABSTRACT

A chip antenna includes a substrate, a plurality of helical conductors provided on the substrate, and a pair of terminals provided on the substrate. One of the plurality of helical conductors is connected electrically to one of the
5 terminals, and another one of the helical conductors is connected electrically to the other terminal. Thus, the antenna is of a small size, yet is a single unit which alone is capable of transmitting and receiving electromagnetic waves of a plurality of frequencies.